

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Hiroshi SUKEGAWA

SERIAL NUMBER: 09/731,788

GROUP: 2187

FILED: December 8, 2000

EXAMINER: CHACE, CHRISTIAN

FOR: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE HAVING PLURAL
MEMORY CIRCUITS SELECTIVELY CONTROLLED BY A MASTER CHIP
ENABLE TERMINAL OR AN INPUT COMMAND AND OUTPUTTING A
PASS/FAIL RESULT



REQUEST TO CORRECT TITLE OF INVENTION

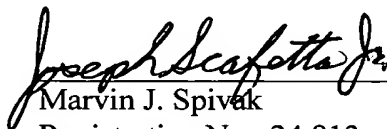
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COMMISSIONER FOR PATENTS
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SIR:

In the matter of the above-identified application for patent, we hereby request
correction of your records to reflect the correct title of the invention. The title of the
invention should read as follows: NONVOLATILE SEMICONDUCTOR MEMORY
DEVICE HAVING PLURAL MEMORY CIRCUITS SELECTIVELY CONTROLLED BY
A MASTER CHIP ENABLE TERMINAL OR AN INPUT COMMAND AND
OUTPUTTING A PASS/FAIL RESULT.

Respectfully Submitted,

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